Eurogam project **EDOC128**

Synoptiques GE,BGO,ROCI

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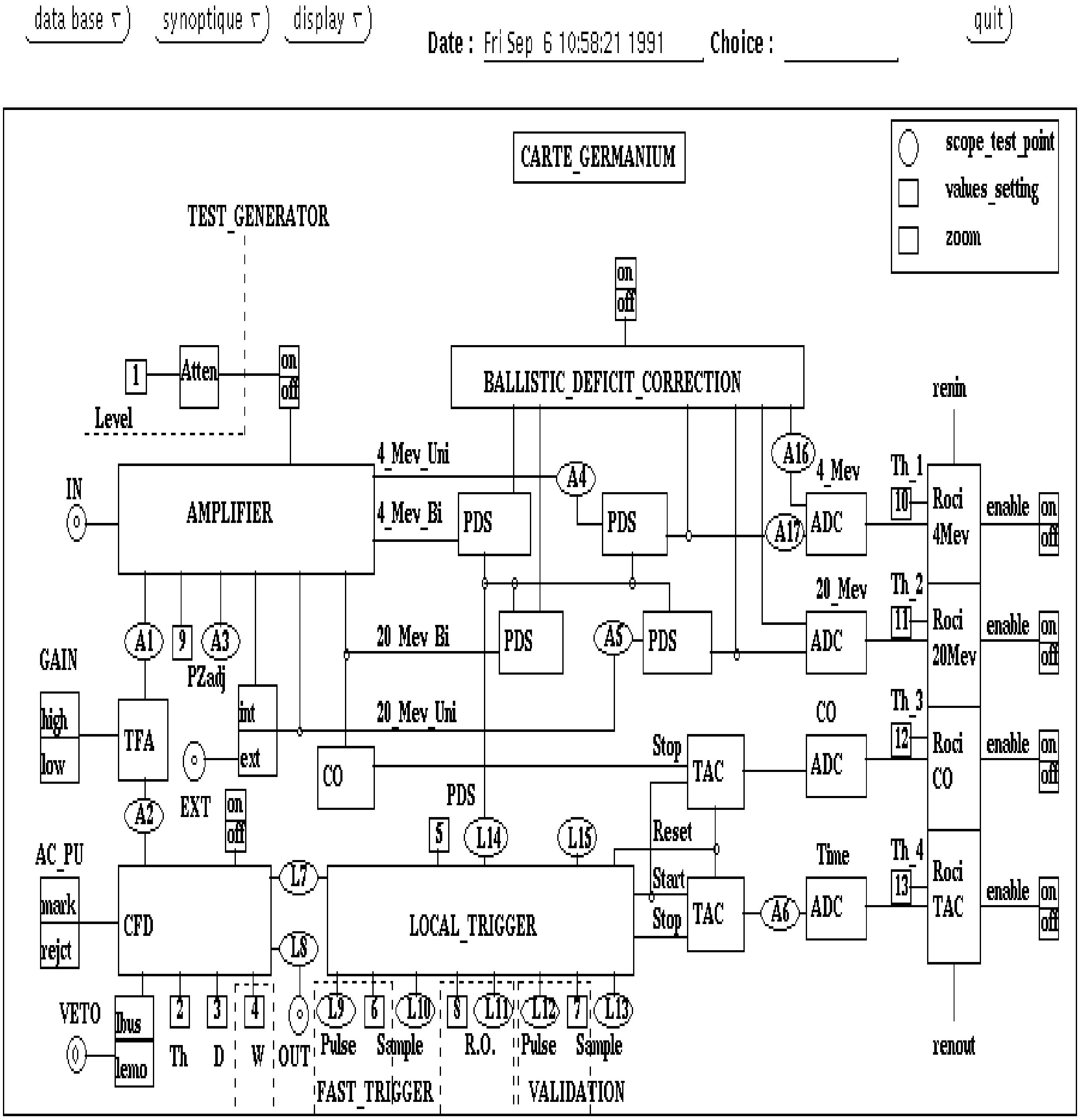
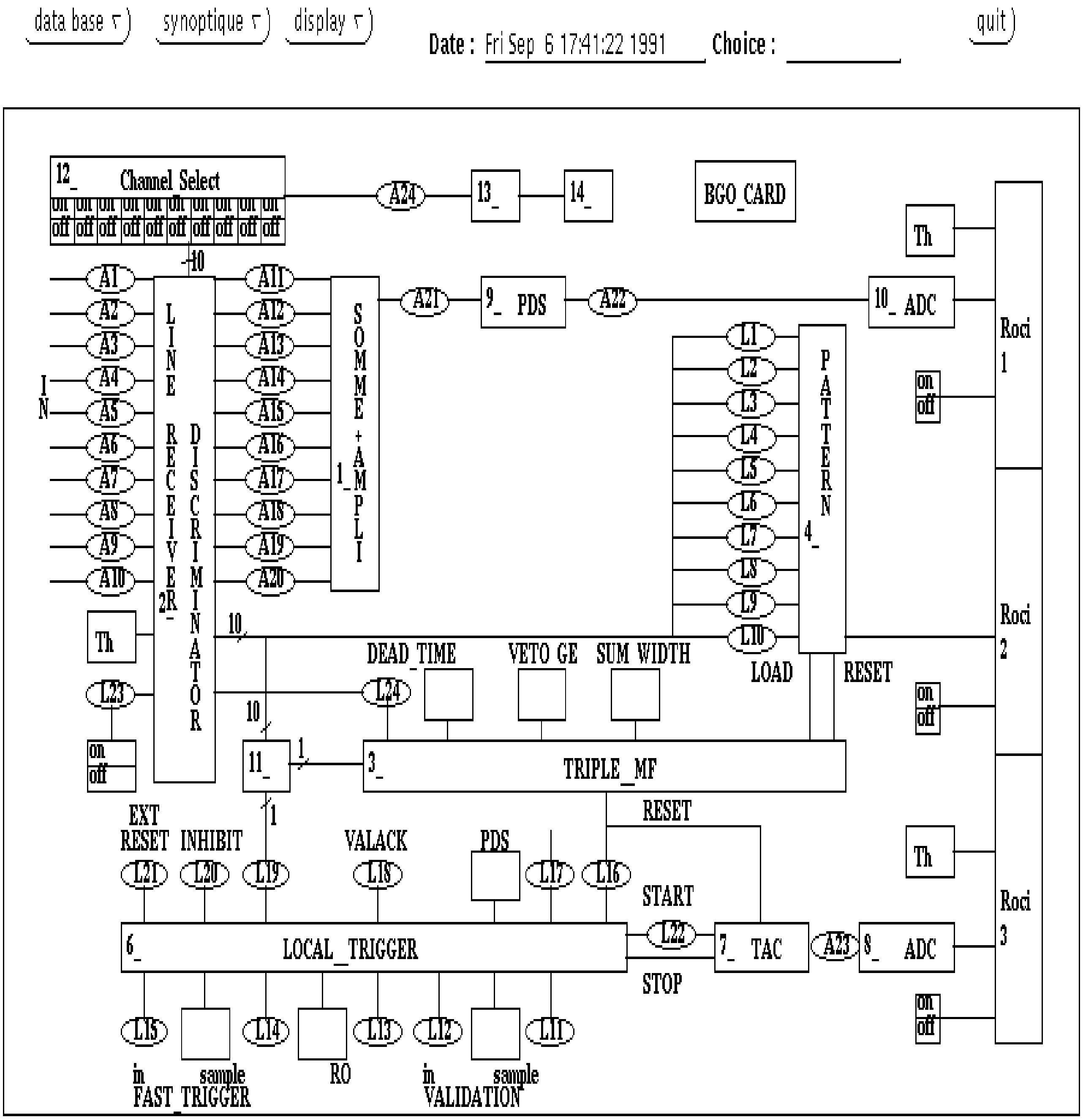
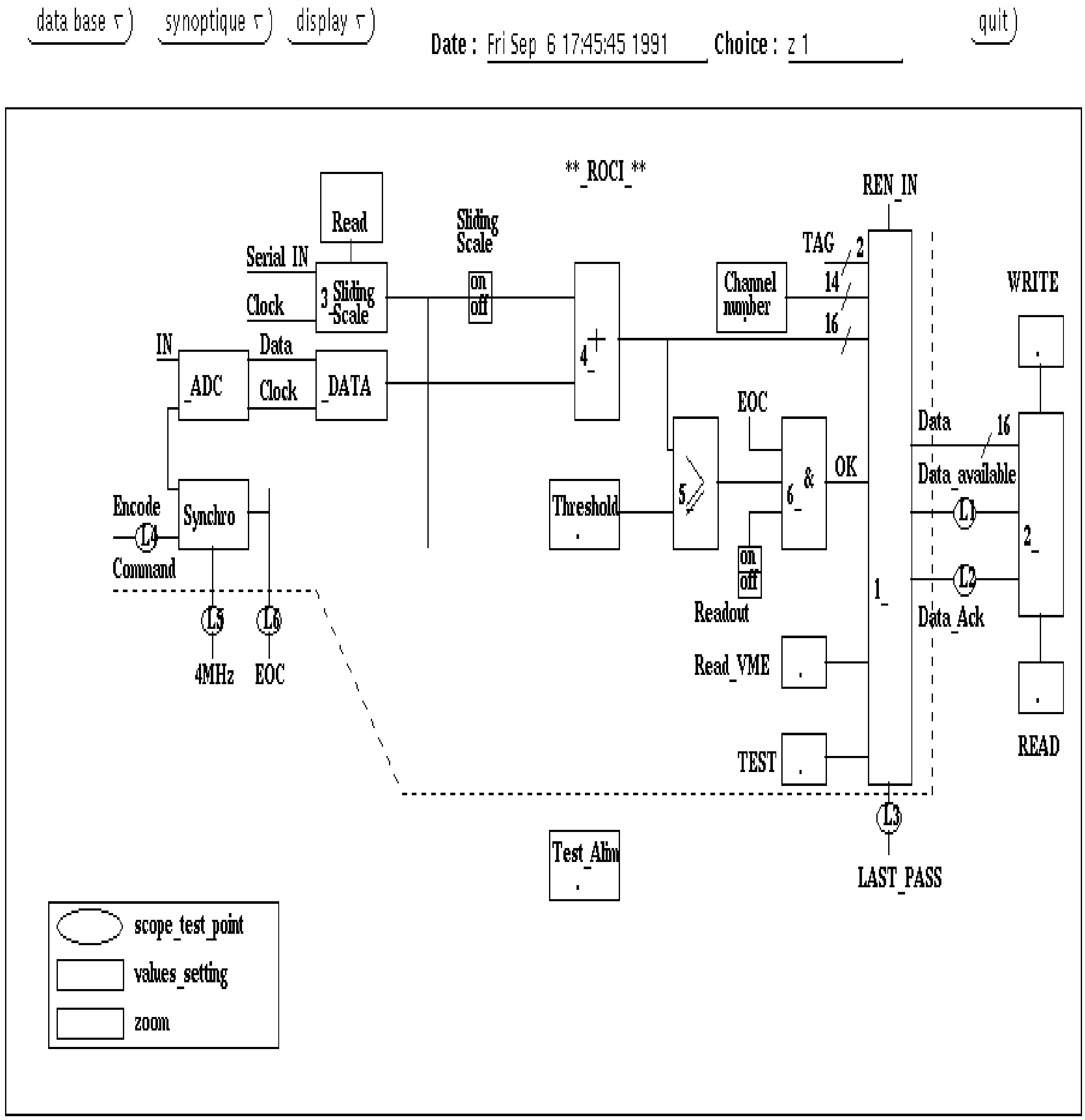

data base
synoptique
display
Date : Fri Sep 6 10:58:21 1991
Choice :
quit
CARTE_GERMANIUM
scope_test_point
values_setting
zoom
TEST_GENERATOR
1
Atten
on
off
Level
BALLISTIC_DEFICIT_CORRECTION
IN
AMPLIFIER
4_Mev_Uni
4_Mev_Bi
PDS
A4
A16
A17
4_Mev
ADC
Th_1
10
Roci
4Mev
enable
renin
20_Mev
Th_2
11
20Mev
GAIN
high
low
A1
9
A3
PZadj
TFA
20_Mev_Bi
A5
20_Mev_Uni
int
ext
EXT
CO
Stop
TAC
Th_3
12
A2
PDS
AC_PU
mark
rejct
CFD
L7
L8
5
L14
L15
Reset
Start
Time
Th_4
13
A6
LOCAL_TRIGGER
VETO
lbus
lemo
2
3
4
Th
D
W
OUT
L9
6
L10
8
L11
L12
7
L13
Pulse
Sample
R.O.
FAST_TRIGGER
VALIDATION
renout


data base
synoptique
display
Date : Fri Sep 6 17:41:22 1991
Choice :
quit
12_ Channel Select
on off
A24
13_
14_
BGO_CARD
Th
10
A1 A2 A3 A4 A5 A6 A7 A8 A9 A10
IN
LINE RECEIVER
DISCRIMINATOR
2_
A11 A12 A13 A14 A15 A16 A17 A18 A19 A20
SOMME + AMPLI
1_
A21
9_ PDS
A22
10_ ADC
L1 L2 L3 L4 L5 L6 L7 L8 L9 L10
PATTERN
4_
Roci 1
Roci 2
Roci 3
DEAD_TIME
VETO GE
SUM WIDTH
LOAD
RESET
L23
L24
11_
3_ TRIPLE_MF
EXT RESET
INHIBIT
VALACK
PDS
L21 L20 L19 L18 L17 L16
START
L22
7_ TAC
A23
8_ ADC
STOP
6_ LOCAL_TRIGGER
L15 L14 L13 L12 L11
in sample
FAST_TRIGGER
RO
in sample
VALIDATION



data base
synoptique
display
Date : Fri Sep 6 17:45:45 1991
Choice : z 1
quit
** _ROCI_ **
Read
Serial IN
Clock
3 _Sliding Scale
Sliding Scale
on
off
IN
_ADC
Data
Clock
_DATA
4 _+
Channel number
REN_IN
TAG
2
14
16
WRITE
EOC
5
6_ &
OK
Data
16
Data_available
Threshold
Encode
L4
Synchro
Command
L5
L6
4MHz
EOC
on
off
Readout
Read_VME
TEST
1_
L1
L2
2_
Data_Ack
READ
L3
LAST_PASS
Test_Alim
scope_test_point
values_setting
zoom