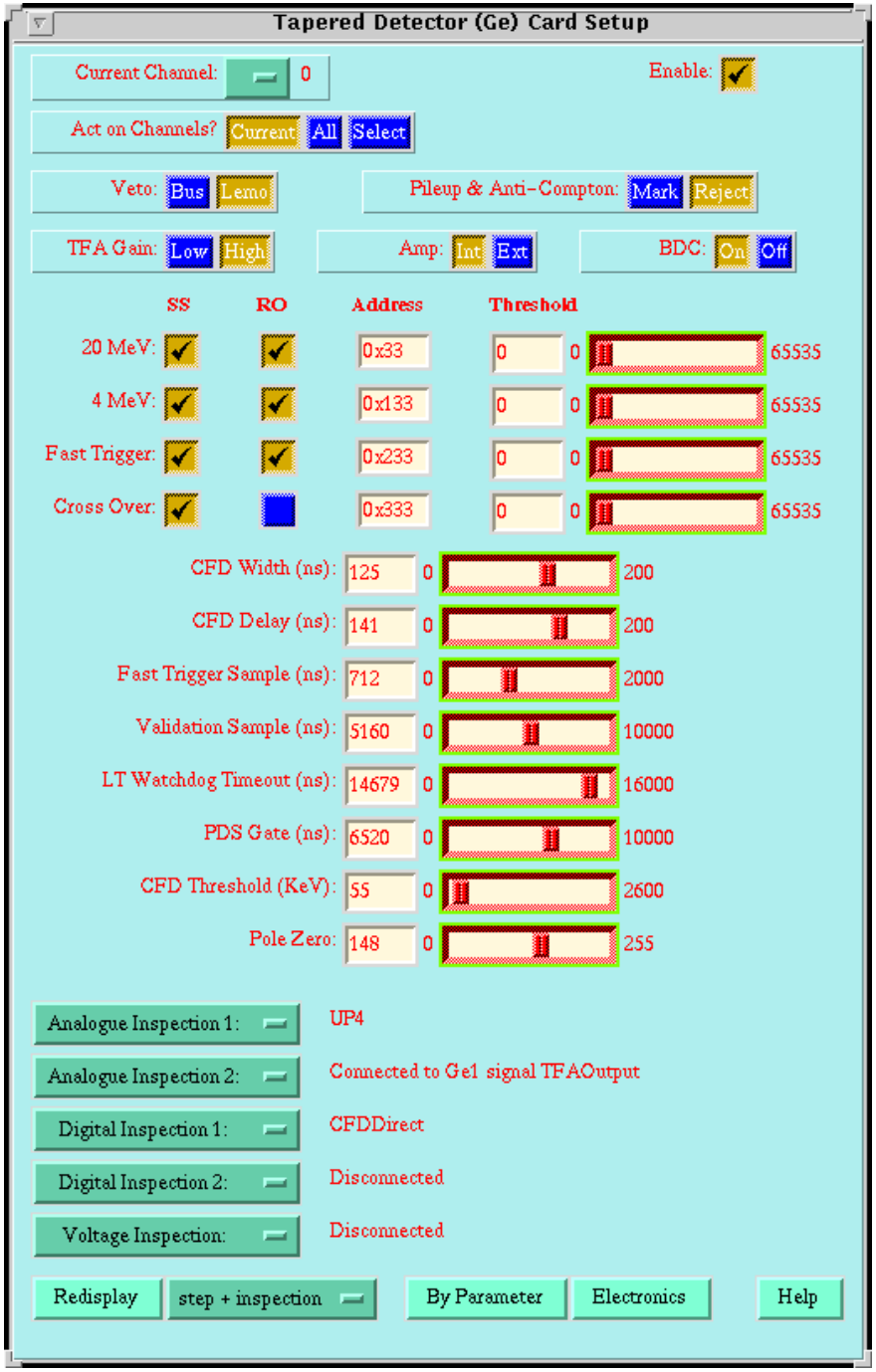
Tapered Detector (Ge) Card Setup
Current Channel: 0
Enable:
Act on Channels? Current All Select
Veto: Bus Lemo
Pileup & Anti-Compton: Mark Reject
TFA Gain: Low High
Amp: Int Ext
BDC: On Off
SS
RO
Address
Threshold
20 MeV: 0x33 0 0 65535
4 MeV: 0x133 0 0 65535
Fast Trigger: 0x233 0 0 65535
Cross Over: 0x333 0 0 65535
CFD Width (ns): 125 0 200
CFD Delay (ns): 141 0 200
Fast Trigger Sample (ns): 712 0 2000
Validation Sample (ns): 5160 0 10000
LT Watchdog Timeout (ns): 14679 0 16000
PDS Gate (ns): 6520 0 10000
CFD Threshold (KeV): 55 0 2600
Pole Zero: 148 0 255
Analogue Inspection 1: UP4
Analogue Inspection 2: Connected to Ge1 signal TFAOutput
Digital Inspection 1: CFDDirect
Digital Inspection 2: Disconnected
Voltage Inspection: Disconnected
Redisplay
step + inspection
By Parameter
Electronics
Help