CDOC218

CHARISSA DATA ACQUISITION SYSTEM

Register Definitions for the LeCroy 2551 Scaler

Edition 1.0

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Nuclear Physics Support Group

Central Laboratory of the Research Councils
Daresbury Laboratory

Registers for the LeCroy 2551 Scaler module

This document should be read in conjunction with the document **EDOC210** which describes the **Register Server** implementation for **CAMAC**.

This is a 12 channel Scaler module with CAMAC data readout.

All register attributes include -c (crate address) and -n (station address)

Registers using the **xCAMAC** class of the **CAMAC** Register Server

Channel data read by f(0) a(x) or f(2) a(x)

Addressed readout of the 12 register values (channels) via the **CAMAC** dataway. Channel x (0-11) is selected using subaddress x.
Readout using f(2) clears the module and LAM on A(11).

lc2551#*.c[0-11].data attributes -a x -f 0 -w 16 -p ro -l 0 -b 0 -z x
lc2551#*.c[0-11].data+clr attributes -a x -f 2 -w 16 -p ro -l 0 -b 0 -z x

Registers using the **cCAMAC** class of the **CAMAC** Register Server

These registers do not require data and have no initial value. However read-register operations may return Q and X as data.

reset write or read by f(9) a(0)

Module reset. Clears all scaler channels simultaneously.

lc2551#*.reset attributes -a 0 -f 9

test LAM read by f(8) a(0)

returns Q=1 if the module LAM is set

lc2551#*.testlam attributes -a 0 -f 8

Disable LAM read by f(24) a(0)

Disables the module LAM. The state of the module LAM enable will be arbitary after power on.

lc2551#*.disable attributes -a 0 -f 24

Enable LAM read by f(26) a(0)

Enables the module LAM.

lc2551#*.enable attributes -a 0 -f 26

Increment scalers read by f(25) a(0)

Increment all scalers.

lc2551#*.increment attributes -a 0 -f 25