CDOC216

CHARISSA DATA ACQUISITION SYSTEM

Register Definitions for the LeCroy 4208 TDC

Edition 1.0

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Registers for the LeCroy 4208 TDC module

This document should be read in conjunction with the document **EDOC210** which describes the **Register Server** implementation for **CAMAC**.

This is a 8 channel TDC module with CAMAC data readout.

All register attributes include -c (crate address) and -n (station address)

Registers using the **xCAMAC** class of the **CAMAC** Register Server

ADC data read by f(0) a(x) or f(2) a(x)

Addressed readout of the 8 ADC values via the **CAMAC** dataway (2's complement convention). Channel x (0-7) is selected using subaddress x. Q=0 is returned if an empty channel is read otherwise Q=1 is returned. Readout using f(2) clears the module at S2.

lc4208#*.adc*.data attributes -a x -f 0 -w 16 -p ro -l 0 -b 0 -z x
lc4208#*.adc*.data+clr attributes -a x -f 2 -w 16 -p ro -l 0 -b 0 -z x

Registers using the **cCAMAC** class of the **CAMAC** Register Server

These registers do not require data and have no initial value. However read-register operations may return Q and X as data.

reset write or read by f(9) a(0)

module reset

lc4208#*.reset attributes -a 0 -f 9

test LAM read by f(8) a(0)

returns Q=1 if the module LAM is set

lc4208#*.testlam attributes -a 0 -f 8

test LAM and clear read by f(10) a(0)

returns Q=1 if the module LAM is set and clears the LAM

lc4208#*.testclearlam attributes -a 0 -f 10