DOC233

MIDAS

Register Definitions for the LeCroy 4532 module

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Registers for the LeCroy 4532 module

This document should be read in conjunction with the document **EDOC210** which describes the Register Server implementation for **CAMAC**.

This is a Majority Logic Unit.

All register attributes include -c (crate address) and -n (station address)

Registers using the **xCAMAC** class of the **CAMAC** Register Server

Input Pattern low read by f(0) a(0)

Read the input pattern, channels 1 to 16.

lc4532#*.pattern.L attributes -a 0 -f 0 -w 16 -p ro -l 0 -b 0

Input Pattern high read by f(0) a(1)

Read the input pattern, channels 17 to 32.

lc4532#*.pattern.H attributes -a 1 -f 0 -w 16 -p ro -l 0 -b 0

Status Register read by f(1) a(0)

Read the status register.

bit 0	LAM	set = 1 if LAM is ON
bit 1	LAM enable	set = 1 if LAM Enable switch is ON
bit 2	MEMORY enable	set = 1 if MEMORY Enable switch is ON
bit 3	CLUSTER enable	set = 1 if CLUSTER Enable switch is ON

lc4532#*.status	attributes -a 0 -f 1 -w 16 -p ro -l 4 -b 0
lc4532#*.status.LAM	attributes -a 0 -f 1 -p ro -l 1 -b 0 -z d
lc4532#*.status.LAMenable	attributes -a 0 -f 1 -p ro -l 1 -b 1 -z d
lc4532#*.status.MEMenable	attributes -a 0 -f 1 -p ro -l 1 -b 2 -z d
lc4532#*.status.Cenable	attributes -a 0 -f 1 -p ro -l 1 -b 3 -z d

Registers using the **cCAMAC** class of the **CAMAC** Register Server

These registers do not require data and have no initial value. However read-register operations may return Q and X as data.

test lam	**write or read by f(8) a(0)**

Test LAM; returns Q=1 if L is ON

lc4532#*.testlam	attributes -a 0 -f 8

clear	**read by f(9) a(0)**

Clears the data memory and LAM

lc4532#*.clear	attributes -a 0 -f 9

test and clear	**read by f(10) a(0)**

Test and Clear LAM; returns Q=1 if L is ON.

lc4532#*.test+clrlam	attributes -a 0 -f 10