DOC232

MIDAS

Register Definitions for the LeCroy 4516 module

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Nuclear Physics Support Group

Central Laboratory of the Research Councils
Daresbury Laboratory

Registers for the LeCroy 4516 module

This document should be read in conjunction with the document **EDOC210** which describes the Register Server implementation for **CAMAC**.

This is an Programmable Logic Unit.

All register attributes include -c (crate address) and -n (station address)

Registers using the **cCAMAC** class of the **CAMAC** Register Server

These registers do not require data and have no initial value. However read-register operations may return Q and X as data.

Set C0 to AND write or read by f(26) a(0)

Sets all C0s to AND mode.

lc4516#*.C0toANDmode attributes -a 0 -f 26

Set C0 to OR write or read by f(24) a(0)

Sets all C0s to OR mode.

lc4516#*.C0toORmode attributes -a 0 -f 24

Set C1 to AND write or read by f(26) a(1)

Sets all C1s to AND mode.

lc4516#*.C1toANDmode attributes -a 1 -f 26

Set C1 to OR write or read by f(24) a(1)

Sets all C1s to OR mode.

lc4516#*.C1toORmode attributes -a 1 -f 24

Test C0 **read by f(27) a(0)**

Returns Q=1 if C0 switch is in AND mode.

lc4516#*.C0inANDmode attributes -a 0 -f 27

Test C1 **read by f(27) a(1)**

Returns Q=1 if C1 switch is in AND mode.

lc4516#*.C1inANDmode attributes -a 1 -f 27