DOC231

MIDAS

Register Definitions for the LeCroy 4504 module

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Registers for the LeCroy 4504 Flash ADC module

This document should be read in conjunction with the document **EDOC210** which describes the Register Server implementation for **CAMAC**.

This is a four channel Flash ADC Module.

All register attributes include -c (crate address) and -n (station address)

Registers using the **xCAMAC** class of the **CAMAC** Register Server

Digitial Output read by f(0) a(0)

Read digitial outputs; 4 bits per channel.

lc4504#*.adc0.data	attributes -a 0 -f 0 -w 16 -p ro -l 4 -b 0
lc4504#*.adc1.data	attributes -a 0 -f 0 -w 16 -p ro -l 4 -b 4
lc4504#*.adc2.data	attributes -a 0 -f 0 -w 16 -p ro -l 4 -b 8
lc4504#*.adc3.data	attributes -a 0 -f 0 -w 16 -p ro -l 4 -b 12

Digitial Overflow read by f(1) a(0)

Read digitial overflows; 1 bit per channel.

lc4504#*.adc0.oflow	attributes -a 0 -f 1 -w 16 -p ro -l 1 -b 0 -z d
lc4504#*.adc1.oflow	attributes -a 0 -f 1 -w 16 -p ro -l 1 -b 1 -z d
lc4504#*.adc2.oflow	attributes -a 0 -f 1 -w 16 -p ro -l 1 -b 2 -z d
lc4504#*.adc3.oflow	attributes -a 0 -f 1 -w 16 -p ro -l 1 -b 3 -z d

Low Reference Voltage write by f(16) a(0)

Write the Low Reference Voltage (VL), 8 bits; range from -2550 mV to +2250 mV in steps of 20 mV.

lc4504#*.VL	attributes -a 0 -f 16 -w 16 -p wo -l 8 -b 0

High Reference Voltage write by f(16) a(1)

Write the High Reference Voltage (VH), 8 bits; range from -2550 mV to +2250 mV in steps of 20 mV.

lc4504#*.VH	attributes -a 1 -f 16 -w 16 -p wo -l 8 -b 0

Registers using the **cCAMAC** class of the **CAMAC** Register Server

These registers do not require data and have no initial value. However read-register operations may return Q and X as data.

C	**write or read by f(25) a(0)**

module C function

lc4504#*.C	attributes -a 0 -f 25