DOC228

MIDAS

Register Definitions for the LeCroy 4518 Module

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Registers for the LeCroy 4518 module

This document should be read in conjunction with the document **EDOC210** which describes the **Register Server** implementation for **CAMAC**.

The LeCroy 4518 (previously 4418) is a Programmable Logic Delay/Fan-Out.

All register attributes include -c (crate address) and -n (station address)

Registers using the **xCAMAC** class of the **CAMAC** Register Server

Delay Settings write by f(16) a(x)

Load delay time setting (4 bits) for channel x using subaddress x.

lc4518#*.delay* attributes -a x -f 16 -w 16 -p wo -l 4 -b 0