# The GREAT Triggerless Total Data Readout Method

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Abstract-- Recoil Decay Tagging (RDT) is a very powerful method for the spectroscopy of exotic nuclei. RDT is a delayed coincidence technique between detectors usually at the target position and at the focal plane of a spectrometer. Such measurements are often limited by dead time. This paper describes a novel triggerless data acquisition method which is being developed for the Gamma Recoil Electron Alpha Tagging (GREAT) spectrometer that overcomes this limitation by virtually eliminating dead time.

Our solution is a Total Data Readout (TDR) method where all channels run independently and are associated in software to reconstruct events. The TDR method allows all the data from both target position and focal plane to be collected with practically no dead time losses. Each data word is associated with a timestamp generated from a global 100MHz clock. Events are then reconstructed in real time in the event builder using temporal and spatial associations defined by the physics of the experiment.

# I. INTRODUCTION

The Gamma Recoil Electron Alpha Tagging (GREAT) spectrometer [1] will be used in nuclear structure physics experiments to analyse the decay particles and gamma rays of recoiling nuclei produced in heavy-ion fusion reactions. Initially it will be used at the focal plane of RITU [2], the recoil separator at Jyväskylä.

Normally it will be used in conjunction with a target position detector array to collect prompt gamma rays or conversion electrons emitted during the initial stages of the decay of the recoiling nucleus. Examples of target position detector arrays to be used with GREAT are VEGA [3] or Jurosphere [4] for gamma rays and SACRED [5] for

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conversion electrons. Fig. 1 shows a generic experimental setup for GREAT at Jyväskylä where an incoming heavy ion beam hits a target in the centre of a detector array sensitive either to conversion electrons or gamma rays. The resulting fusion products pass through the magnets which act as a recoil separator to select certain mass-charge-energy combinations for transmission to the GREAT detectors at the focal plane.

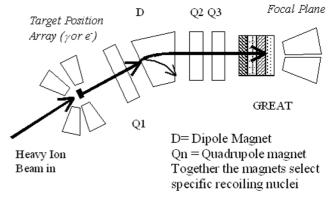


Fig. 1: Typical GREAT experiment configuration at Jyväskylä with a target array detecting either gamma rays or conversion electrons and a recoil separator

The GREAT spectrometer uses 5 focal plane detector types:

- 28 Silicon PIN diodes for conversion electrons,
- Silicon (Si) strip detectors (2-d, 60x40) for recoils and for alpha and beta decays,
- A Multi-Wire Proportional Chamber (MWPC) for recoil detection and identification
- A planar Germanium (Ge) strip detector (2-d 24x12) to detect betas and low energy photons
- A segmented large volume Ge detector for higher energy gammas

Recoil Decay Tagging (RDT) [6] experiments collect data at 3 distinct stages: firstly prompt radiation at the target position, secondly recoiling nuclei as they emerge from the recoil separator into GREAT, and thirdly emissions from decays of implanted nuclei in the GREAT spectrometer's detectors. Each of these three stages contributes to the triggering.

The delay between the prompt radiation and detection of recoils in GREAT depends on the mass and energy of the recoiling nucleus and also the characteristics of the spectrometer. Times of flight range from 0.5 to 5µs. Emissions of gamma rays, alphas, betas or electrons from implanted nuclei in the Si strip detectors can take place over a wide range of times from a few nanoseconds to hundreds of seconds. The correlation between the decaying implanted nuclei and the other data is made positionally and relies on having only one recoil per pixel in the Si strip detectors within the correlation interval. The correlation of recoils with their subsequent decays is at the heart of the RDT technique.

## II. CONVENTIONAL RDT TRIGGERING

Up to now RDT experiments have normally used a common dead time data acquisition system where a trigger condition might typically be that any focal plane detector is active. This occurs some microseconds after the radiation is emitted at the target position because of the recoil's time of flight through the separator. The time of flight delay is overcome by using delayed coincidence mode in the ADCs instrumenting the target array. This means that the target position ADCs are gated whenever a recoil is detected at the focal plane at a rate of up to 10kHz (assuming 10% efficiency for transmission to focal plane and 100kHz target rate). Singles data at the target position are collected separately for monitoring purposes, but not written to tape. The gate width depends on the decay lifetime of the implanted nuclei. Typically gate widths of up to 5µs are used. This period is sufficient to include recoil implantations at the focal plane and decay of short lived isomers to the ground state as well as gammas at the target. (Focal plane alpha and beta ground state decays take place much later and are recorded in separate events.) During the gate period all ADCs which fire are converted and written to tape along with a single timestamp based on the start of the gate. Both good data and randoms (rubbish) are written to tape at a rate of up to 10kHz (rate depends on the type of reaction under study).

Dead time can be calculated assuming a rate of 5kHz at the focal plane in the Silicon detector and allowing  $35\mu s$  for the gate and for readout of the ADCs. In this case we have a dead time of 18%, rising to 35% if the rate in the Silicon detector increases to 10kHz. Obviously longer gates increase the dead time still further.

Longer gates are desirable for studying isomeric decays where, for example, an isomer with a half life of  $10\mu s$  requires a gate of  $40\text{-}50\mu s$ . However a  $50\mu s$  gate, with readout time added, and a rate of 10kHz leads to a dead time of 75% and a  $50\mu s$  gate incorporates a lot of random gammas from the target position's delayed coincidence

Another problem related to dead time is that during readout the focal plane electronics is dead which means that any further alpha or beta ground state emissions are missed. These are rare events, sometimes only a few per day, so missing even one is a serious drawback. In some experiments the fixed "blind" period at the end of the gate during readout can be a problem although normally gates are adjusted to include all interesting lifetimes.

To summarise, the problems with the normal RDT triggering method are (1) wide gates (over  $10\mu s$ ) are desirable from the physics perspective, but are precluded by dead time; causing loss of rare ground state emission events from implanted nuclei at the focal plane (2) the data written to tape include both good and random data which must later be separated and this problem gets worse with wider gates. The extra random data extend the readout time and also increase the data rate to tape by increasing the data volume.

To overcome the dead time problem and to select only good data to be written to tape the GREAT electronics will use a triggerless data collection method called Total Data Readout.

### III. TOTAL DATA READOUT (TDR)

The Total Data Readout (TDR) method overcomes the dead time limitations by reading all the data, timestamping them, and then collecting the event fragments together in software in the event builder using spatial and temporal correlations.

The correlation in the event builder can be very complex or quite simple. A simple example would eliminate random gamma rays by starting with recoils, and then looking back in time using a window centred on the time of flight (± some jitter period). All gamma rays detected during that window would be considered to be correlated with the recoil. At the same time the x,y co-ordinate of the implanted nucleus would be recorded and later emissions from that location can be correlated and treated as implant decays.

A more complex example would require that both the recoil-gamma condition and the implant decay condition are true within some defined time (based on the implanted nucleus' decay half-life) before data are recorded.

Software triggering like that described here clearly requires a large amount of memory and processing power in the event processing system. Our estimates indicate that TDR has recently become feasible as a result of the increasing power commercially available in affordable CPUs.

## IV. TDR ELECTRONICS AND DATA ACQUISITION

Fig. 2 shows a schematic overview of the GREAT electronics and data acquisition system. In this section of the paper the various parts of the system are described.

The front end electronics (Constant Fraction Discriminators (CFD), and shaping amplifiers) comprises commercial NIM/CAMAC units. The Timestamping ADC card is a new design in VXI-D format offering 32 independent channels (14 bits with sliding scale correction and zero suppression). The ADC conversion and readout time is less than the shaping amplifier's busy period, so the only counting losses are those due to pileup. A gate is generated for each input either from its associated external CFD, or using a software controlled gate matrix which permits a single input to start a group of gates, for example one wire in the MWPC to gate itself and its immediate neighbours. The timestamp is recorded for each channel at

the start of its gate. In the event of pileup, a second timestamp is recorded to indicate the second firing of the CFD within the original gate period. Qualifiers mark data which is affected by pileup or coincident with a front panel veto input.

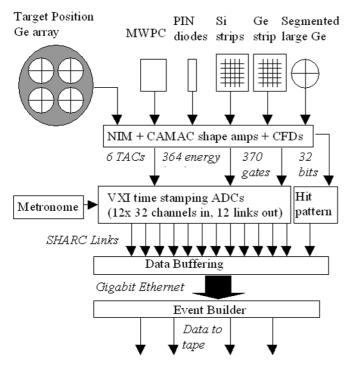


Fig. 2 Schematic diagram of the GREAT TDR System's electronics and data acquisition

The timestamping requires the distribution and synchronisation of a 100MHz clock. The clock distribution takes advantage of the facilities built into D-sized VXI, viz. a 100MHz clock, ClockSynch signal (to select one edge of the clock) and two STAR lines which arrive at each slot within a crate with less than 2ns skew. A VME module known as the Metronome controls the clock distribution and maintains synchronisation of all the ADCs.

A hit pattern VME module provides a method to include patterns in the data stream along with a timestamp. Hit patterns may be either made up of all the bits which are active at some time during the gate input (gated) or they may be transitional. Transitional patterns generate a timestamp whenever an input bit becomes active. Transitional pattern mode may be either gated or free running. Conventional clocked hit patterns are also possible by using a very narrow gate to sample the inputs in gated mode. The operation of the pattern modules is controlled from software with individual enable/disable mask bits for each input.

The timestamped data words from the ADCs and the pattern units are each tagged with an address to identify which detector they come from and which signal they represent within that detector. The data items from both ADCs and pattern cards are then sent in time-order via point to point data links (SHARC DSP links [7]) into VME cards where SHARC DSPs receive and buffer the information ready for use by the event builder. Currently the DSPs are used only for data moving, but there is spare

processing capacity which could be used later for real time diagnostic data histogramming. Every ADC card and pattern module has its own SHARC data link. Each link can cope with up to 50k gates/second on all 32 of its associated ADC inputs. The ADC cards have a diagnostic scaler for missed counts which indicates whether the SHARC data links and their buffers ever filled to the point of blocking data collection.

Data are transferred from the buffers to the event builder over Gigabit Ethernet. Output data from the event reconstruction processors in the event builder are sent to tape for storage using one or more data streams where each data stream corresponds to one class of reconstructed event. In a typical RDT experiment there could, for example, be 3 streams: one for the prompt recoil-gamma data (gamma ray spectroscopy), one for the emission from the implanted nuclei (decay spectroscopy) and one for events where items from the first two streams have been successfully correlated (Recoil Decay Tagging).

#### V. STATUS

The GREAT project is funded and under construction in the UK. The system design is complete and reported here. The event builder design has started and the electronics design is taking place during 2000/1. Detectors are on order and it is anticipated that the system will be assembled and tested during 2001 for experiments in 2002.

#### VI. CONCLUSIONS

The TDR method opens new possibilities for the study of exotic nuclei by allowing the collection of very rare events in a system that simultaneously collects high rate data without dead time problems. The only losses are due to pileup in the shaping amplifiers which limits the rate on any one channel. If GREAT works as expected then in the future TDR will become the architecture of choice for large nuclear structure projects and hardware trigger systems will become obsolete.

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